UTILITY PATENT APPLICATIO TRANSMITTAL Progressional applications under 37 CFR 1.53(b)

Attorney Docket No. 214019US2

First Inventor or Application Identifier

Assignee Address:

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NONVOLATILE SEMICONDUCTOR MEMORY AND METHOD OF FABRICATING THE SAME

Assignee Name: KABUSHIKI KAISHA TOSHIBA

Assistant Commissioner for Pate Box Patent Application Washington, DC 20231

APPLICATION ELEMENTS
MPEP chapter 600 concerning utility patent application contents ADDRESS TO:

			VVasimigion, DO 20201				
	Fee Transmittal Form (e.g. F (Submit an original and a duplicate	PTO/SB/17) for fee processing)	ACCOMPANYING APPLICATION PARTS				
			Assignment Papers (cover sheet & document(s))				
 7 .9	Specification	Total Sheets 21	8. Application Data Sheet. See 37 CFR 1.76				
			9. 37 C.F.R. §3.73(b) Statement Power of Attorne				
3.	Formal Drawing(s) (35 U.S.C. 113)	Total Sheets 10	10. □ English Translation Document (if applicable)				
			11. ■ Information Disclosure Statement (IDS)/PTO-1449 ■ Copies of IDS Citations (1)				
4.	Oath or Declaration	Total Pages 3	12. □ Preliminary Amendment				
tand	a. Newly executed (original	ıl)	13. ■ White Advance Serial No. Postcard				
	b. □ Copy from a prior applic	ation (37 C.F.R, §1.63(d)) I w/ box 17 completed)	14. Certified Copy of Priority Document(s) (1) (if foreign priority is claimed)				
	i. DELETION OF IN' Signed statement attact the prior application, se 1.33(b).	VENTOR(S) thed deleting inventor(s) named in the 37 C.F.R. §1.63(d)(2) and	15. ☐ Applicant claims small entity status.				
5. □	CD-ROM or CD-R in duplica Program (Appendix)	te, large table or Computer	16. ■ Other: Notice of Priority				
6. ⊏	Nucleotide and/or Amino Aci (if applicable, all necessary)	id Sequence Submission					
a. □ Computer Readable Form (CRF)							
b. Specification or Sequence Listing on:							
<u>.</u>	i. □ CD-ROM or CD-R	(2 copies); or					
F.3	ii. □ Paper						
Aug. 1	c. Statements verifying ide	entity of above copies					
17 If	a CONTINUING APPLICATIO	N check appropriate box and supp	only the requisite information below:				
	17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below: □ Continuation □ Divisional □ Continuation-in-part (CIP) of prior application no.:						
		Examiner:	Group Art Unit:				
	For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is						
considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.							

18. Amend the specification by inserting before the first line the sentence:

☐ This application is a ☐ Continuation of application Serial No.

□ Division Filed on

☐ Continuation-in-part (CIP)

□ Which was published in English

□ Which was not published in English

☐ This application claims priority of provisional application Serial No.

Filed

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214019US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Eiji SAKAGAMI

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

NONVOLATILE SEMICONDUCTOR MEMORY AND METHOD OF FABRICATING THE SAME

FEE TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	21 - 20 =	1	× \$18 =	\$18.00
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☐ MULTIPLE DEPENDENT CLAIMS (If applicable) + \$270 =				\$0.00
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Respectfully Submitted,

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MAIER & NEUSTADT, P.C.

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